

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S19 2	61	"713"/151.ccls. and (("PCMCi" "NIC" interface network) near1 (card adpater circuit board device apparatus chip)) same ((encrypt\$3 cipher\$3 scrambl\$3 cypher\$3 decrypt\$3 decipher\$3 unscrambl\$3 uncipher\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:45
S19 4	0	S192 and ((send\$3 assert\$3 transmit\$4 insert\$3 affirm\$3 call\$3 invok\$3) near2 (interrupt))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:00
S19 5	2	"20030046585"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:01
S19 6	169	"713"/150-154,162,189.ccls. and (("PCMCi" "NIC" interface network) adj1 (card adpater circuit board device apparatus chip)) with ((encrypt\$3 cipher\$3 scrambl\$3 cypher\$3 decrypt\$3 decipher\$3 unscrambl\$3 uncipher\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:13
S19 7	8	("6760799" "6993613" "6868466" "6968411"). pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:09
S19 9	0	S196 and ((send\$3 assert\$3 transmit\$4 insert\$3 affirm\$3 call\$3 invok\$3) near2 (interrupt) with (wait delay latency period interval))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:14
S20 0	182	"713"/150-154,160,162,189.ccls. and (("PCMCi" "NIC" interface network) adj1 (card adpater circuit board device apparatus chip)) with ((encrypt\$3 cipher\$3 scrambl\$3 cypher\$3 decrypt\$3 decipher\$3 unscrambl\$3 uncipher\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:46
S20 1	0	S200 and ((send\$3 assert\$3 transmit\$4 insert\$3 affirm\$3 call\$3 invok\$3) near2 (interrupt) with (wait delay latency period interval))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:17
S20 2	2	S200 and ((send\$3 assert\$3 transmit\$4 insert\$3 affirm\$3 call\$3 invok\$3) near2 (interrupt))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:14
S20 3	23	726/11,12,14.ccls. and (("PCMCi" "NIC" interface network) adj1 (card adpater circuit board device apparatus chip)) with ((encrypt\$3 cipher\$3 scrambl\$3 cypher\$3 decrypt\$3 decipher\$3 unscrambl\$3 uncipher\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:18

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S20 4	0	726/11,12,14.ccls. and ((send\$3 assert\$3 transmit\$4 insert\$3 affirm\$3 call\$3 invok\$3) near2 (interrupt) with (wait delay latency period interval))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:48
S20 5	12	726/11,12,14.ccls. and ((send\$3 assert\$3 transmit\$4 insert\$3 affirm\$3 call\$3 invok\$3) near2 (interrupt))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:18
S20 6	98	709/224,230,220,227.ccls. and (("PCMCi" "NIC" interface network) adj1 (card adpater circuit board device apparatus chip)) with ((encrypt\$3 cipher\$3 scrambl\$3 cypher\$3 decrypt\$3 decipher\$3 unscrambl\$3 uncipher\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:18
S20 7	0	S206 and ((send\$3 assert\$3 transmit\$4 insert\$3 affirm\$3 call\$3 invok\$3) near2 (interrupt) with (wait delay latency period interval))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:44
S20 8	3	S206 and ((send\$3 assert\$3 transmit\$4 insert\$3 affirm\$3 call\$3 invok\$3) near2 (interrupt))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:19
S20 9	143	710/260-264.ccls. and ((send\$3 assert\$3 transmit\$4 insert\$3 affirm\$3 call\$3 invok\$3) near2 (interrupt) with (wait delay latency period interval))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:44
S21 0	0	S209 and (("PCMCi" "NIC" interface network) near1 (card adpater circuit board device apparatus chip)) same ((encrypt\$3 cipher\$3 scrambl\$3 cypher\$3 decrypt\$3 decipher\$3 unscrambl\$3 uncipher\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:53
S21 1	1	"713"/150-154,160,162,189.ccls. and (((insert\$3 assert\$3 add\$3 input\$3) near2 (interrupt\$3)) same ((transfer\$3 FORWARD\$3 send\$3 moving move transmitt\$3) with (encrypt\$3 cipher\$3 cypher\$3 scrambl\$3 decrypt\$3 decipher\$3 uncipher\$3 unscrambl\$3)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:47
S21 2	0	726/11,12,14,2,3.ccls. and (((insert\$3 assert\$3 add\$3 input\$3) near2 (interrupt\$3)) same ((transfer\$3 FORWARD\$3 send\$3 moving move transmitt\$3) with (encrypt\$3 cipher\$3 cypher\$3 scrambl\$3 decrypt\$3 decipher\$3 uncipher\$3 unscrambl\$3)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:48
S21 3	3	"370"/\$.ccls. and (((insert\$3 assert\$3 add\$3 input\$3) near2 (interrupt\$3)) same ((transfer\$3 FORWARD\$3 send\$3 moving move transmitt\$3) with (encrypt\$3 cipher\$3 cypher\$3 scrambl\$3 decrypt\$3 decipher\$3 uncipher\$3 unscrambl\$3)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:49

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L7	42	380/255,266,42,37.ccls. and (("PCMCi" "NIC" interface network) adj1 (card adpater circuit board device apparatus chip)) with ((encrypt\$3 cipher\$3 scrambl\$3 cypher\$3 decrypt\$3 decipher\$3 unscrambl\$3 uncipher\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 15:16
L8	0	l7 and ((send\$3 assert\$3 transmit\$4 insert\$3 affirm\$3 call\$3 invok\$3) near2 (interrupt) with (wait delay latency period interval))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 15:13
L20	7	(secondar\$4 near3 interrupt\$3 with (decrypt\$3 decipher\$3 uncipher\$3 unscrambl\$3 descrambl\$3 security))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 15:20
S21 4	10	((((insert\$3 assert\$3 add\$3 input\$3) near2 (interrupt\$3)) and ((transfer\$3 FORWARD\$3 send\$3 moving move transmitt\$3) with (encrypt\$3 cipher\$3 cypher\$3 scrambl\$3 decrypt\$3 decipher\$3 uncipher\$3 unscrambl\$3))).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 15:14
S21 5	1	((("PCMCi" "NIC" interface network) near1 (card adpater circuit board device apparatus chip)) and ((encrypt\$3 cipher\$3 scrambl\$3 cypher\$3 decrypt\$3 decipher\$3 unscrambl\$3 uncipher\$3)) and (((insert\$3 assert\$3 add\$3 input\$3) near2 (interrupt\$3)) and ((transfer\$3 FORWARD\$3 send\$3 moving move transmitt\$3) with (encrypt\$3 cipher\$3 cypher\$3 scrambl\$3 decrypt\$3 decipher\$3 uncipher\$3 unscrambl\$3))))).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/11 11:54

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Results 11 - 20 of about 120 for decryption "secondary use" interrupt. (0.10 seconds)EP1100269 Sony european software patent - Contents receiving ...

a means (step) for **decrypting** an encrypted style sheet. .... against each hardware component and a process corresponding to each hardware **interrupt**. ...  
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Contents receiving system and contents receiving method - US ...

A TS decoder 53 **interrupts** the transport stream and separate it into AV data .... has unscrambling key data can primary and **secondary use** the style sheet. ...  
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STUX GNU/LINUX Software List

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PACKAGES.TXT; Wed Jun 27 16:26:16 EEST 2007 This file provides ...

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mirror.inode.at/data/bluewhite64/bluewhite64-11.0/PACKAGES.TXT - 415k -

<http://www.google.com/search?q=decryption+%22secondary+use%22+interrupt&hl=en&r...> 9/11/2007


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decryption +"secondary use" +interrupt

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**1 [Ada development system technical and performance requirements \(with rationale\)](#)**


Donald G. Krantz

 December 1990 **Proceedings of the conference on TRI-ADA '90 TRI-Ada '90**

Publisher: ACM Press

 Full text available: [pdf\(1.85 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#)

This paper discusses requirements for Ada1 compilers and associated tools used for real-time embedded weapons systems (EWS) development. The requirements have been developed over a period of several years by embedded systems developers at Honeywell Inc. and Alliant Techsystems Inc. Requirements for the run time system, compiler-generated code, and host tools such as linkers are presented. A short rationale statement is provided with each specific requirement.

**2 [Privacy in pervasive environments: next generation labeling protocols](#)**


Mark S. Ackerman

 November 2004 **Personal and Ubiquitous Computing**, Volume 8 Issue 6

Publisher: Springer-Verlag

 Full text available: [pdf\(221.64 KB\)](#) Additional Information: [full citation](#), [abstract](#), [citations](#), [index terms](#)

In pervasive environments, privacy is likely to be a major issue for users, and users will want to be notified of potential data capture. To provide notice to users, this paper argues for what it calls labeling protocols, technical mechanisms through which users can be informed of data requests and their consequences. Recent experiences with the Platform for Privacy Preferences Project (P3P), an attempt to provide privacy mechanisms for the Web, suggest important lessons for the design of a n ...

**Keywords:** Labeling protocols, P3P, Pervasive environments, Platform for privacy preferences, Privacy, Ubiquitous computing

**3 [Performance Evaluation and Monitoring](#)**


Henry Lucas

 September 1971 **ACM Computing Surveys (CSUR)**, Volume 3 Issue 3

Publisher: ACM Press

 Full text available: [pdf\(1.10 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Three major purposes for evaluating the hardware and software performance of computer systems--selection evaluation, performance projection, and performance monitoring--are described. Eight techniques that have been used or suggested for evaluating performance are discussed. Each of these techniques is rated on its suitability for the three purposes of evaluation. Recommendations are made on the most appropriate technique for each


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IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. Simple error detection methods for hardware implementation of Advance Standard  
Chih-Hsu Yen; Bing-Fei Wu;  
[Computers, IEEE Transactions on](#)  
Volume 55, Issue 6, June 2006 Page(s):720 - 731  
Digital Object Identifier 10.1109/TC.2006.90  
[AbstractPlus](#) | Full Text: [PDF](#)(2176 KB) IEEE JNL  
[Rights and Permissions](#)
- ☐ 2. A fault-tolerant architecture for symmetric block ciphers  
Min-Kyu Joo; Jin-Hyung Kim; Yoon-Hwa Choi;  
[Test Symposium, 2002. \(ATS '02\). Proceedings of the 11th Asian](#)  
18-20 Nov. 2002 Page(s):212 - 217  
Digital Object Identifier 10.1109/ATS.2002.1181713  
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